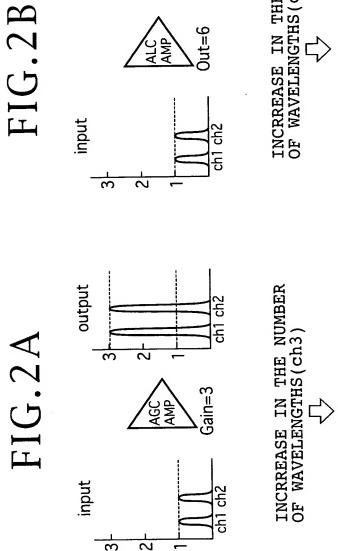
$2(\mathtt{CLT}) \sim$ PostAMP/ MUX PreAMP/ DMUX mhh 4(#4) 4(#3) SΚ ΝS PostAMP/ MUX PreAMP/ DMUX FIG. 1 PostAMP/ MUX PreAMP/ DMUX 4(#2) 4(#2) ΝS SW PostAMP/ MUX PreAMP/ DMUX 14 PostAMP/ MUX PreAMP/ DMUX immi) 10 4(#6) ΝS SW 4(#1) PostAMP/ MUX PreAMP/ DMUX ∞



output



ch1 ch2

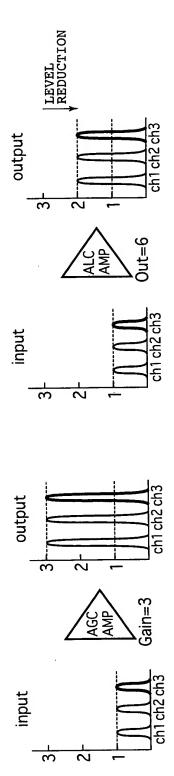
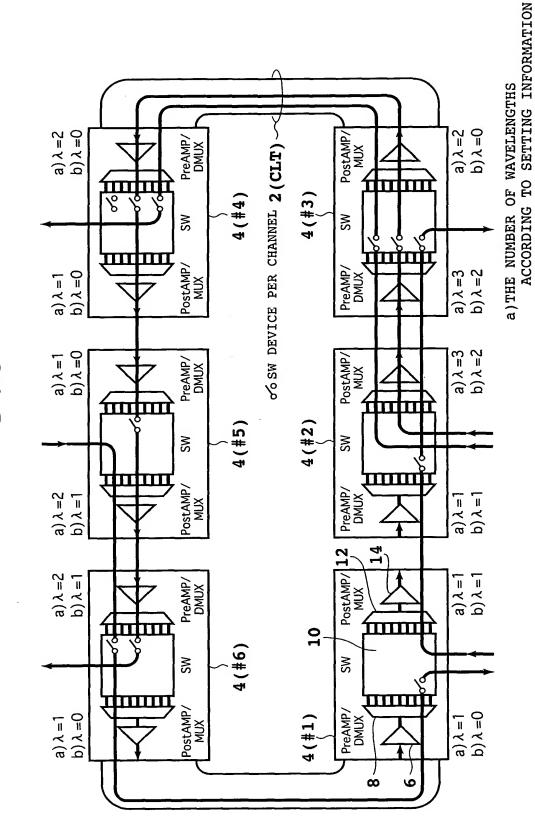


FIG.3



b) THE NUMBER OF WAVELENGTHS
ACCORDING TO SWITCH RESTRICTION

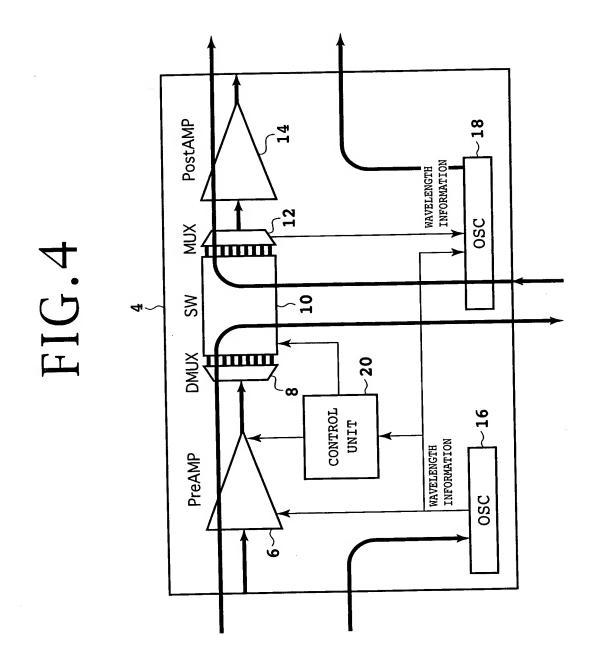
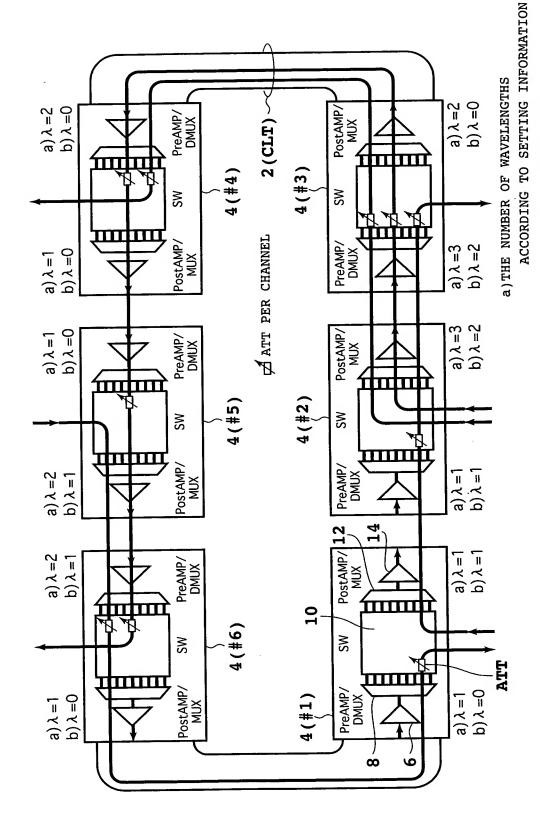
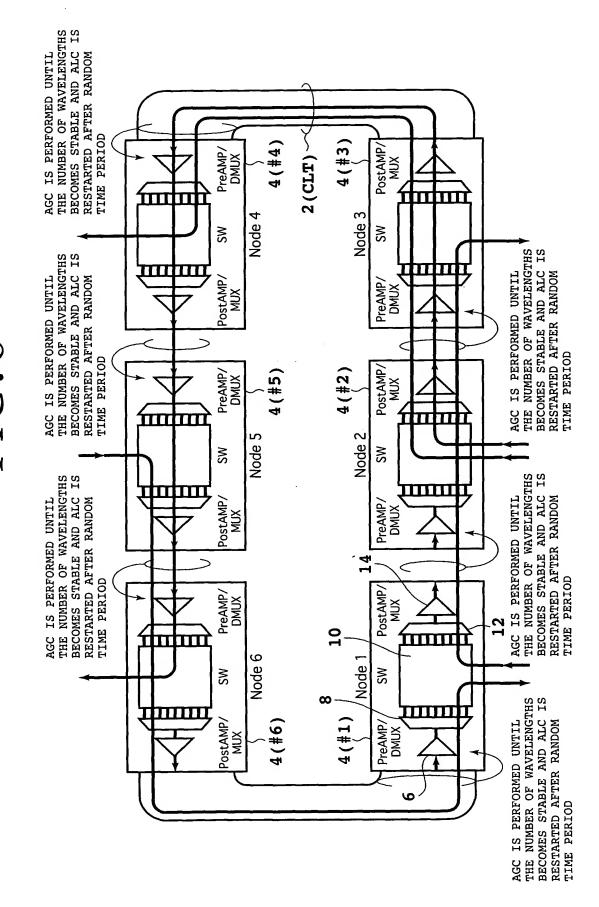


FIG.5



b) THE NUMBER OF WAVELENGTHS
ACCORDING TO ATT RESTRICTION

FIG.6



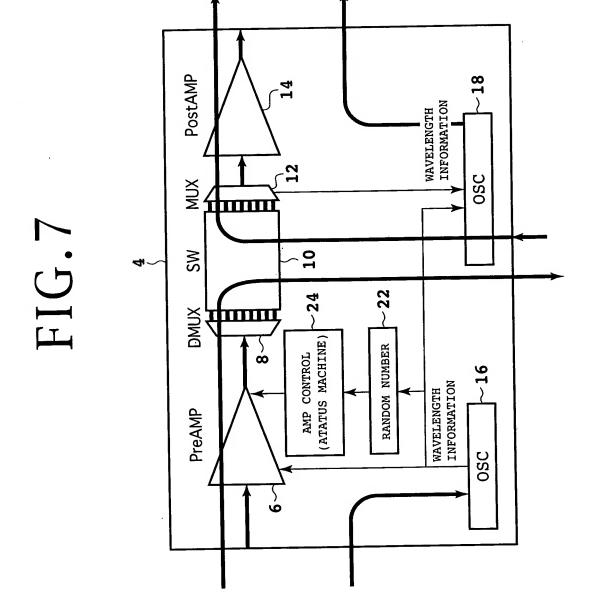


FIG.8

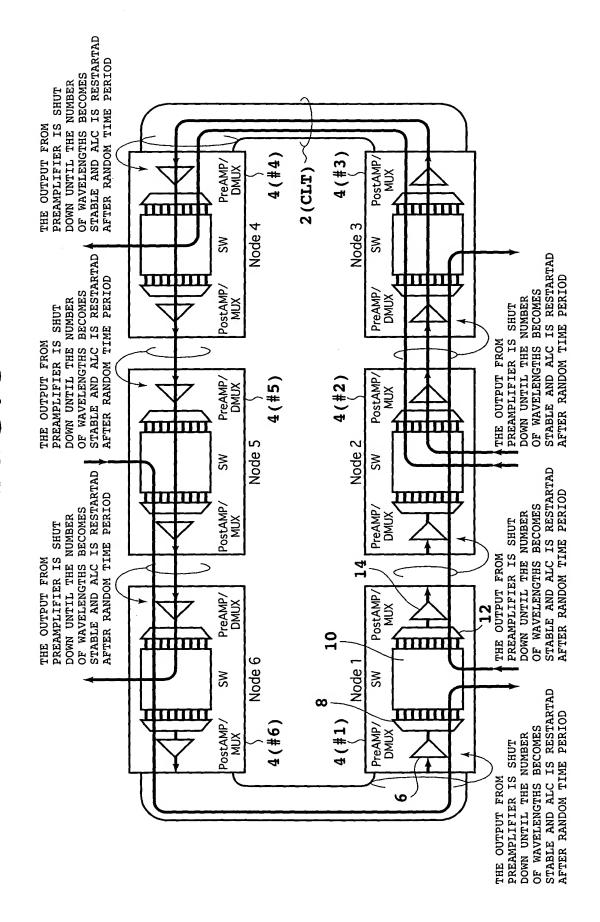
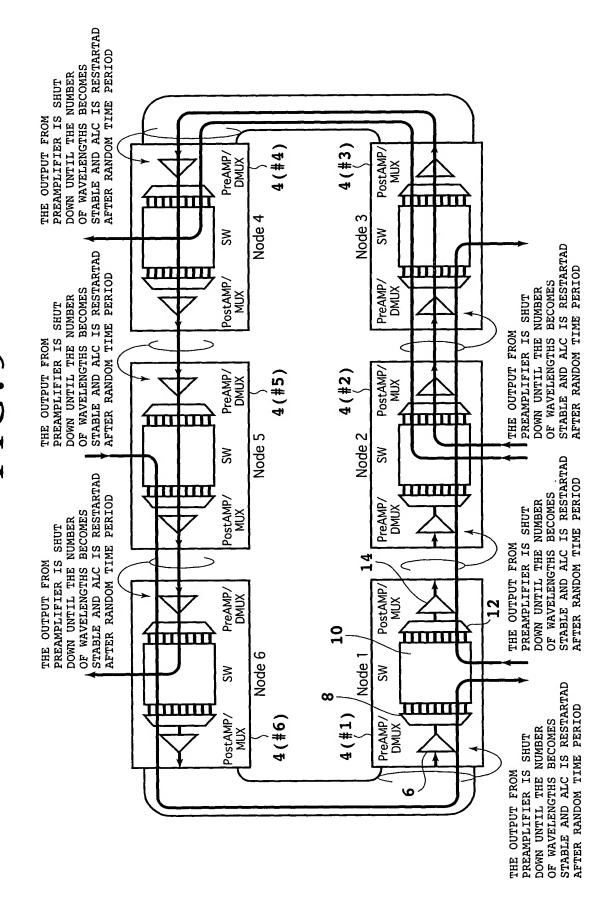


FIG.9



PostAMP WAVELENGTH INFORMATION FIG.10 osc MUX 10 SW \sim 24 ~26 DMUX (STATUS MACHINE) **%** SPECIFIC VALUE AMP CONTROL \sim 16 WAVELENGTH INFORMATION PreAMP osc